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APPLICANTS

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** CONTINUING DATA ******Yes TD*

This application is a CON of 08/803,145 02/19/1997 PAT 6,282,506

** FOREIGN APPLICATIONS ******Yes TD*

JAPAN 8-032152 02/20/1996

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 05/15/2001

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance		DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged _____ Examiner's Signature	<i>Murphy</i>	<i>TD</i> Initials	26	11 4	10 4

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TITLE

METHOD OF DESIGNING SEMICONDUCTOR INTEGRATED CIRCUIT UTILIZING A SCAN TEST FUNCTION

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)
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